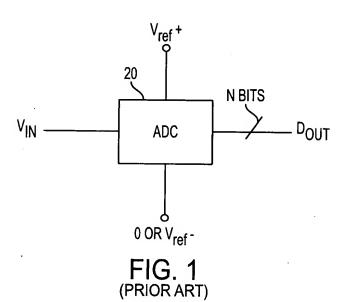
Applicant: Michael K. Mayes

Application No.:To Be Assigned
Filed: Herewith
For: CIRCUITS AND METHODS FOR A VARIABLE OVERSAMPLE RATIO DELTA-SIGMA ANALOG-TO-DIGITAL CONVERTER

LT-155CON



30 35 **B BITS M BITS OVERSAMPLED** LOW-PASS ANALOG DELTA- $V_{IN}$ - D<sub>OUT</sub> DIGITAL FILTER SIGMA **MODULATOR** F<sub>OUT</sub> = F<sub>SAMPLE</sub>/OSR - OSR  $\mathsf{F}_{\mathsf{SAMPLE}}$ 

FIG. 2 (PRIOR ART)

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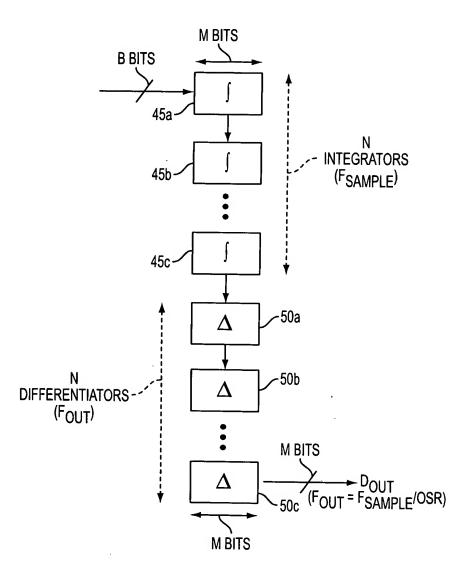


FIG. 3 (PRIOR ART)

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VARIABLE OVERSAMPLE RATIO DELTASIGMA ANALOG-TO-DIGITAL CONVERTER

LT-155CON

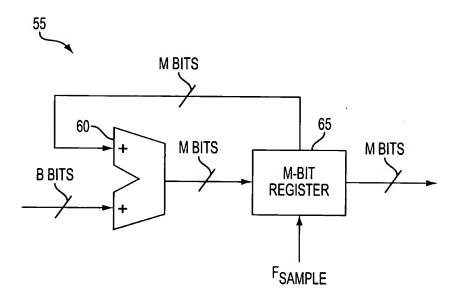


FIG. 4A (PRIOR ART)

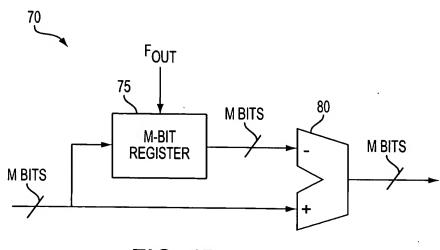


FIG. 4B (PRIOR ART)

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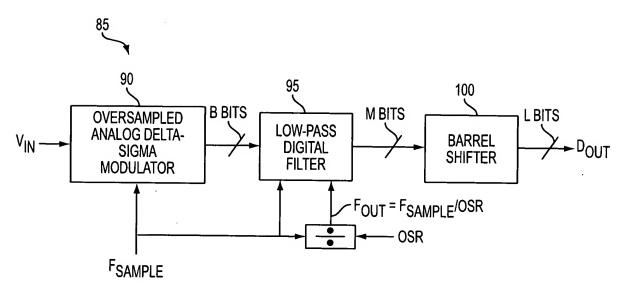


FIG. 5 (PRIOR ART)

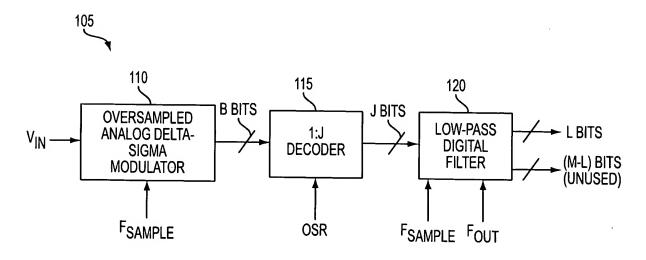


FIG. 6

Application No.:To Be Assigned Filed: Herewith

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VARIABLE OVERSAMPLE RATIO DELTASIGMA ANALOG-TO-DIGITAL CONVERTER

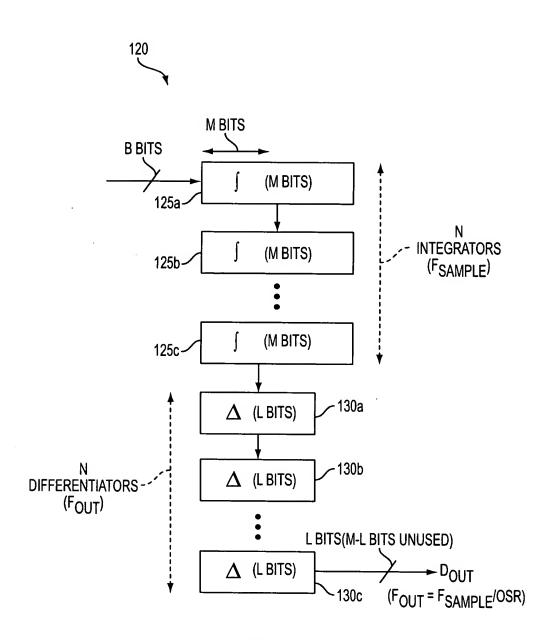


FIG. 7